Digital Lock-In Amplifiers

OE1201-DSP Lock-In Amplifier



Features

- 50 mHz to 120 kHz frequency range
- Low-noise current and voltage inputs
- 1 nV to 1 V full-scale sensitivity
- Time constants extending from 10 μs to 3 ks
- Up to 120 dB dynamic reserve
- Up to 100dB CMRR

Overview

OE1201 Small-sized DSP Lock-in Amplifier provides a superb performance within its bandwidth from 50 mHz to 120 kHz. With the high-speed 4-core architecture, high-precision ADC and excellent analog performance, OE1201 can easily detect the phase and the magnitude of weak signals overwhelmed by various large noise. The performance of OE1201 is as good as other lock-in amplifiers all over the world, even better than them in some certain parameters, such as measurement accuracy, operating bandwidth , which meets the needs of scientific research and industrial application well.

Input Channel

OE1201 detects an input signal in a single-ended mode or a differential voltage mode. With an ultra low-noise preamplifier, the input noise is as low as 9 nV/ $\sqrt{\text{Hz}@997}$ Hz. The input impedance is 10 M Ω and the full-scale input voltage sensitivity ranges from 1 nV to 1 V. Besides, OE1201 can be used for current measurement with gains of 10⁶ or 10⁸ V/A. Two line filters (50/60 Hz and 100/120 Hz) are designed to eliminate power frequency interference. A programmable gain amplifier is used to adjust the dynamic reserve of the system, so that OE1201 can keep a high dynamic reserve of 120 dB.

Reference Channel

The reference signal can work in external mode or internal mode. In internal mode, a precise and stable internal oscillator generates sine wave as an internal reference that is multiplied by the input signal. This internal signal is without any phase noise. With the digital phase-shifting technique, the phase resolution of the reference signal is 0.01 deg. OE1201 can work at any fixed frequency from 50 mHz to 120 kHz in this mode. In external mode, the reference signal can be a sine wave or a TTL pulse or a square wave. The

rising or falling edge of the external reference signal triggers the Phase Lock Loop (PLL) to lock the external signal. Based on the frequency of the reference signal, the OE1201 can detect the harmonics of the input signal. The maximum harmonic signal frequency can reach 32,767 times the fundamental frequency, and the maximum harmonic frequency cannot exceed the maximum operating frequency of the instrument by 120 kHz.

Digital Demodulator and Output Filter

The key component of the OE1201 is the digital demodulator. Compared to traditional analog lock-in amplifiers, the OE1201's internal digital demodulator effectively rejects the measurement errors caused by DC drift and offset. In addition, by optimizing the multiplication of the internal coherent signal of the digital demodulator, the calculation error is minimized so that the instrument can accurately detect the input weak signal. Time constants of the output low-pass filter from 10 µs to 3 ks can be selected with a choice of 6, 12, 18 or 24dB/oct rolloff. This low-pass digital filter is implemented using a high performance digital filter with a sample rate of 485 kHz. The digital demodulation and the low-pass filter used in OE1201 guarantees a high dynamic reserve (>120dB), accurate phase (absolute phase error <1 deg). Moreover, when the frequency of the input signal is lower than 20 Hz, A synchronous filter can be used to eliminate the harmonic influence of the reference signal, ensuring that OE1201 can detect a low-frequency signal quickly and effectively.

Display

Oe1201 has a 3.5-inch 320 x 240 color TFT-LCD. The measurement results of OE1201, such as X, Y, R, and θ , are shown in numerical form.



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Sens:100m	W Filter:24d	B/oct	Notch: N	IONE	Input
IC: 300mS	DR:Normal Sy	mc:OFF	Cp1:AC	Src: A	Ref
R =	- 2	45.	456	mV	Phase
θ =	- :	0.	456	0	Gain TC
X =	= Z	45.	456	mV	Disp
Y =		0	456	mV	Channel
		<u>.</u>	-00		Output
Overload:	NONE NONE	Freq	1000.00	00Hz	\mathbf{U} +;1;+ \mathbf{v}
Ref. Sour	ce: External	PLL:	LOCKED		ULIILY

Internal Oscillator

The internal oscillator of OE1201 generates a low distortion (-80 dBc) sine reference signal varying from 50 mHz to 120 kHz, which has a high frequency resolution of 1 mHz. The frequency and amplitude of the reference signal can be set by using the front panel of OE1201 or communication interface. When OE1201 is set in the external reference mode, the internal reference signal is phase-locked with the external reference signal.

R = 12.456 mV $X = 12.456 mV$	$\theta = 0.46^{\circ}$ Y = -0.456 mV	Input Filter
Phase: + 0	.00°	Ref
Ref. Source:	Gain	
Freq: 10		
Slope: TTL	Disp	
Harmonic:	Channel Output	
Overload: NONE NONE	Freq: 1000.000Hz	
Ref.Source: External	PLL: LOCKED	JUTILITY

Signal Generator

OE1201 uses a high precision digital-to-analog converter (DAC) to output a sine wave signal at the same frequency as the internal reference signal. The amplitude and phase of the output sine wave can be set through the OE1201's display, where the maximum amplitude of the sine wave is 1 Vrms.

Manual Operation

The parameters are convenient to be adjusted by the soft keys besides the display and the numeric keypad on the front panel, such as the internal oscillator frequency and the SINE OUT amplitude.

Interface

OE1201 uses 9-pin RS-232 and RS-232 to USB interfaces as standard. Through communication interfaces, all instrument functions can be controlled and all data can be read in real-time. Meanwhile, all interfaces of OE1201 are distributed on the front panel and the rear panel.

Remote Operation

Users can use PC to control OE1201 through communication interfaces, including setting the parameters and reading the measurement data. OE1201 is equipped with a free LabVIEW program, which makes it easy to use in complex scientific experiments.

连接中··· 串口配置信息 ∛ASRL3: ▼	连接成功,设备信息: SSI LIA-OE1201 SN:L6040000 Ver1.3	SSI SCIE	I NTIFIC RUMENTS	0E1201 数字锁机 控制平台	相放大器 台
 協入信号源 単純和匠魚等 电流增益 1 Mohn Current 論入展表技地 第2 输入机合、 第3 第4 1 Mohn Zurent 第5 第5 第4 第6 第5 16 16	<u>参考相位(*)</u> 000 <u>参考信号変</u> (所形参考 外部参考信号表型) (可用平上印命 内部参考信号表型 (日本) (日本) (日本)	R值X值Y位 0.05 0.04 0.03 0.02 0.01 0 0	월 θ 值 频率		清除波形 500
 配置输入信号 輸出CH2 輸出CH2 構発(%) の数大 数大 1 	 配置参考信号 着波 加置赤波检測 正弦幅値 (Vrms) 1000 和面子政論出 	R (V) 4.022E-2 X (V) 4.021E-2 300 ms 300 ms 动态储备	θ (°) -1.33 Y (V) -9.328E-4 S 24 d8/oct 灵敏度	页率 (Hz) 199. 997 同步滤被器 关闭	保存数据 数据采样率(S)0.1 配置滤波器 动态储备



OE1201 Specifications

Signal Channel

Voltage input Mode Full-scale Sensitivity

Current input Impedance Voltage

Current C.M.R.R by 6 dB/oct Dynamic reserve Gain accuracy Noise

Reference Channel

Input Frequency range Reference input Input impedance Phase Resolution Absolute phase error Relative phase error Phase noise Internal ref. 1 kHz External ref. time constant, 12 dB/oct) Drift

Harmonic detection (n<32767) Acquisition time Internal Ref. External Ref. whichever is larger

Demodulator

Stability Digital outputs Display Analog outputs reserve settings Harmonic rejection Time constants 1 nV to 1 V in a 1-2-5 sequence 1 fA to 1 μ A 10⁶ or 10⁸ V/A 10 MΩ // 25 pF,

Single-ended or Differential

AC or DC coupled 1 k Ω to virtual ground >100 dB to 10 kHz, decreasing

>120 dB
0.2% typ, 1% max
9 nV/√Hz at 997 Hz
0.14 pA/√Hz at 997 Hz
0.13 pA/√Hz at 97 Hz
50/60 Hz and 100/120 Hz
BNC shield can be grounded

50 mHz to 120 kHz TTL or Sine 1 $M\Omega//25$ pF

1 μdeg <1 deg <1 mdeg

Synthesized, <0.0001 deg at

0.005 deg at 1 kHz (100 ms

<0.1 deg/°C below 10 kHz <0.5 deg/°C above 10 kHz 2F, 3F, ...nF to 120 kHz

Instantaneous acquisition (2 cycles + 5 ms) or 40 ms,

no zero drift on all setting no zero drift on all setting <5 ppm/°C for all dynamic

-90 dB 10 μs to 3 ks (< 200 Hz) 10 μs to 30 s (> 200 Hz) (6, 12, 18, 24 dB/oct rolloff) Synchronous filters

Available below 20 Hz

50 mHz to 120 kHz

-80 dBc (f<10 kHz),

-70 dBc (f>10 kHz)

0.10 Vrms to 1 Vrms

Sine output on rear panel

TTL sync output on rear panel

 $2 \text{ ppm} + 10 \mu \text{Hz}$

1 mHz

1%

100 ppm/°C

Internal Oscillator

Frequency Range Accuracy Resolution Distortion Amplitude Accuracy Stability Outputs

Display

Screen	3.5 inch, 320×240 TFT
Display quantities	4 channels of data to display
	X,Y,R and θ
Display types	Numerical form

Outputs

CH1 and CH2 Outputs	
Function	Χ, Υ, Ρ, θ
Output Voltage	± 5 V full scale.
	30 mA max output current

Interfaces

RS-232 and RS-232 to USB interfaces.

General

Power requirements	
Voltage	220/240 V AC
	100/120 V AC
Frequency	50/60 Hz
Power	20 W
Dimensions	
Width	259 mm
Depth	320 mm
Height	
With feet	115 mm
Without feet	102 mm
Weight	3.2 kg

